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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/651,385	08/29/2000	Sanjay Dabral	042390.P5258D	9681	
7:	590 08/17/2005	EXAM	EXAMINER		
Blakely Sokoloff Taylor & Zafman LLP 12400 Wilshire Boulevard Seventh Floor			DIAZ, J	DIAZ, JOSE R	
Los Angeles, (			ART UNIT	PAPER NUMBER	
<b>Q</b> ,			2815		
			DATE MAILED: 08/17/2005	5	

Please find below and/or attached an Office communication concerning this application or proceeding.

## Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)	
09/651,385	DABRAL ET AL.	
Examiner	Art Unit	
José R. Díaz	2815	

Delote the Filling of all Appeal Direct	Examiner	Art Unit					
	José R. Díaz	2815					
The MAILING DATE of this communication appe	ears on the cover sheet with the c	correspondence add	ress				
THE REPLY FILED 01 August 2005 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.							
The reply was filed after a final rejection, but prior to or of this application, applicant must timely file one of the folking places the application in condition for allowance; (2) a N (3) a Request for Continued Examination (RCE) in comparison.	on the same day as filing a Notice of owing replies: (1) an amendment, a otice of Appeal (with appeal fee) in	of Appeal. To avoid al offidavit, or other evid- compliance with 37 (	ence, which CFR 41.31; or				
following time periods:  a) The period for reply expiresmonths from the mailing a	date of the final rejection						
The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.							
Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).							
Extensions of time may be obtained under 37 CFR 1.136(a). The date on peen filed is the date for purposes of determining the period of extension a CFR 1.17(a) is calculated from: (1) the expiration date of the shortened stabove, if checked. Any reply received by the Office later than three montherarned patent term adjustment. See 37 CFR 1.704(b).	and the corresponding amount of the fee. atutory period for reply originally set in the	The appropriate extension of (2)	on fee under 37 as set forth in (b)				
The Notice of Appeal was filed on A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).							
AMENDMENTS							
The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will <u>not</u> be entered because (a) They raise new issues that would require further consideration and/or search (see NOTE below);							
(a) They raise new issues that would require further companies the issue of new matter (see NOTE below.		71 E below),					
(c) ☐ They raise the issue of new matter (see NOTE bear (c) ☐ They are not deemed to place the application in bear appeal; and/or	etter form for appeal by materially r	educing or simplifying	the issues for				
(d) They present additional claims without canceling a	corresponding number of finally re	ejected claims.					
NOTE: See Continuation Sheet. (See 37 CFR 1.	116 and 41.33(a)).						
4. The amendments are not in compliance with 37 CFR 1.	121. See attached Notice of Non-C	ompliant Amendmen	t (PTOL-324).				
5. Applicant's reply has overcome the following rejection(s	s):						
<ol> <li>Newly proposed or amended claim(s) would be a the non-allowable claim(s).</li> </ol>	allowable if submitted in a separate	e, timely filed amendr	nent canceling				
7. Solution For purposes of appeal, the proposed amendment(s): a how the new or amended claims would be rejected is proposed.		vill be entered and an	explanation of				
The status of the claim(s) is (or will be) as follows:							
Claim(s) allowed: Claim(s) objected to:	·						
Claim(s) rejected: <u>20-23 and 26-36</u> .							
Claim(s) withdrawn from consideration:							
AFFIDAVIT OR OTHER EVIDENCE							
<ol> <li>The affidavit or other evidence filed after a final action, to because applicant failed to provide a showing of good a and was not earlier presented. See 37 CFR 1.116(e).</li> </ol>	nd sufficient reasons why the affida	avit or other evidence	is necessary				
9. The affidavit or other evidence filed after the date of filin entered because the affidavit or other evidence failed to showing a good and sufficient reasons why it is necessar	overcome <u>all</u> rejections under apperry over and was not earlier presented.	eal and/or appellant fa See 37 CFR 41.33(d)	ails to provide a (1).				
10. ☐ The affidavit or other evidence is entered. An explanati REQUEST FOR RECONSIDERATION/OTHER	on of the status of the claims after	entry is below or atta	ched.				
11.   The request for reconsideration has been considered b	ut does NOT place the application	in condition for allow	ance because:				
12. Note the attached Information Disclosure Statement(s)  13. Other:							
	` \	lam I has	meo				
		THOMAS					
		PATENT EXAMINER					

Continuation of 3. NOTE: With regards to claims 20-23, 30-33, and 35-36, the proposed amendment is considered to be proper to overcome the rejection presented in the last Office Action. However, the proposed amendment will not be enter since the amended claims 26 and 34 include the new limitation of a plurality of unit diodes which requires further consideration and/or search.

With regards to claims 27-29, Applicant argues that Marum et al. does not teach or suggest a Z-well (34) separated from the well of the performance circuit (20). However, this argument is not persuasive. Marum et al. shows a circuit layout of the protection circuit (40), which does not include the performance circuit (20) [see figures 3c and 3d]. Thus, the preferred embodiment of Marum et al. clearly teaches a performance circuit (20) formed separated from the protection circuit (40).

In addition, Marum et al. teaches that the performance circuit is a CMOS device [col. 3, II. 27-30], which inherently includes a well. Therefore, the well of the CMOS device is also formed separated from the wells of the protection circuit (40) since both devices, as stated before, are formed separated from each other.

As such, the rejection is considered to be proper since the combination of references makes obvious the claimed limitations...